

JC 1061

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

10760 U.S. PTO
10/000143
10/23/01

APPL NUM 10000143	FILING DATE 10/23/2001	CLASS 365	SUBCLASS	GAU 2813	EXAMINER R. ELMORE
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2187

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:
FRANCE 00/13684 10/25/2000

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO Barret-1
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	
Verified and Acknowledged Examiners's initials			

TITLE : Control of the access to a memory integrated with a microprocessor
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Assistant Examiner		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
Primary Examiner		Print Fig.	
Application Examiner		PREPARED FOR ISSUE	
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